

#17
8/26/04
Mulline

Inventor: Gurtej S. Sandhu et al.

Title: Methods of Forming Transistors

Assignee: Micron Technology, Inc.



INFORMATION DISCLOSURE STATEMENT

References – See Attached Form PTO-1449

The attached form PTO-1449 is submitted in compliance with 37 CFR § 1.56. Copies of the cited art are included. No admission is made regarding whether all the submitted references are prior art.

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AUG 30 2002
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Respectfully submitted,

Dated:

August 26, 2002

Attorney:

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